This document can handle board shapes up to 7.65in x 250in, although

our panel vendors top out at 7in x 250in.

To re-size the board shape, do the following:

Select lines on ME Board Outline and delete (easy in single layer mode...ctrl+tab)

Draw a rectangle using lines (example will be for a 4 " x 6" board

Enter Place Line mode (keyboard pl)

keyboard jo to jump to origin, hit enter

keyboard j1 to jump to location, set x to 4000, hit enter twice

keyboard j1 to jump to location, set x to 4000 and y to -6000, hit enter twice

keyboard j1 to jump to location, set x to 0 and y to -6000, hit enter twice

keyboard jo to jump to origin, hit enter

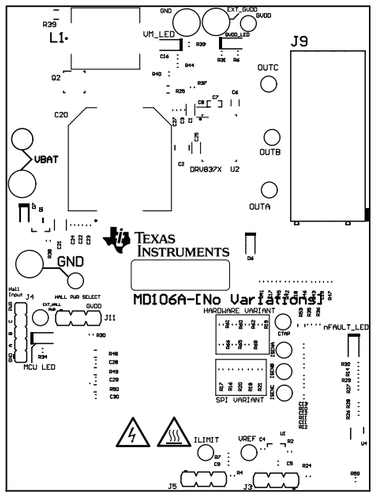
Hit ESC twice to exit place line mode

Select lines on ME Board Outline

Menu DesignBoard Shape>Define from Selected Objects (keyboard dsd)

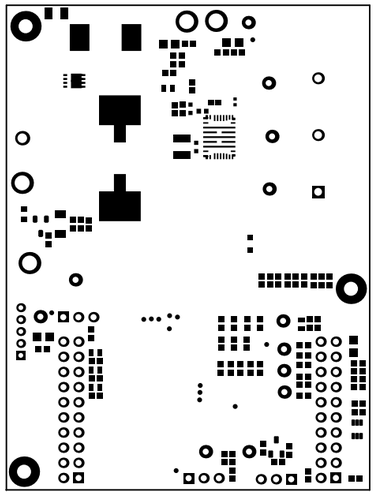
If you re-size the board, don't forget to move the drill table strings on the

Drill Drawing Layer...they should be just to the right of your board shape

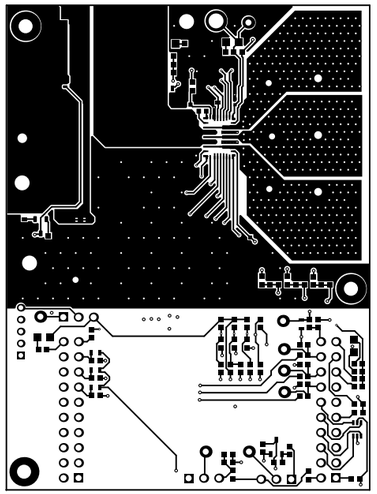


SH-J9 SH-J2

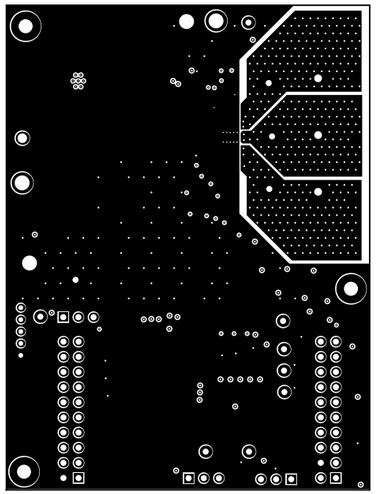
ALL ARTWORK VIEWED FROM TOP SIDE		BOARD #:	MD106	REV: A	SVN REV: Not in version control
LAYER NAME =	Top Overlay	TID #:	N/A		
PLOT NAME =	Top Overlay	GENERATED:	4/23/2026	1:19:11 PM	TEXAS INSTRUMENTS



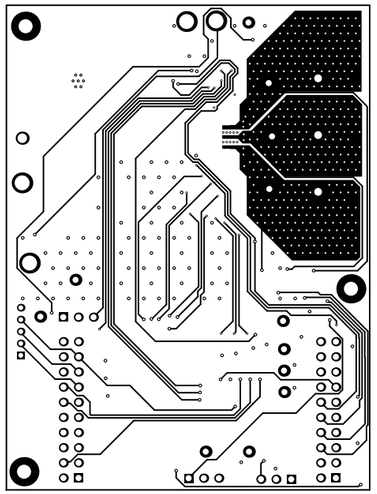
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD N: PCB06	REV: A	SUB REV: Not in version control
LAYER NAME = Top Solder	TID N: N/A		
PLOT NAME = Top Solder Mask	GENERATED: 4/23/2026 5:19:11 PM		TEXAS INSTRUMENTS



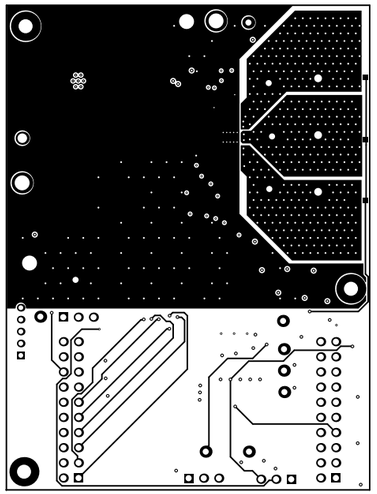
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD N PCB36	REVs A	SVN REVs Not in version control
LAYER NAME = Top Layer	TID N N/A		
PLOT NAME = Top Layer	GENERATED 4/29/2026 3:19:11 PM		TEXAS INSTRUMENTS



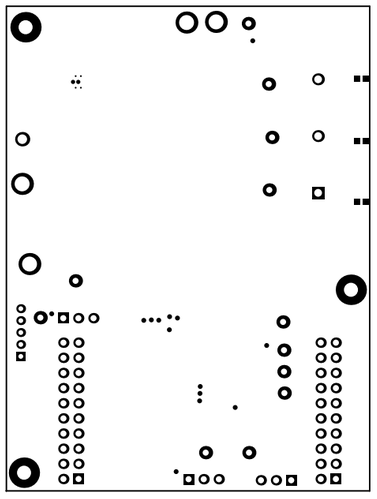
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PCB06	REV: A	SUM REV: Not in version control
LAYER NAME =	TID #: N/A		
PLOT NAME = Signal Layer 1	GENERATED: 4/29/2026 3:18:11 PM		TEXAS INSTRUMENTS



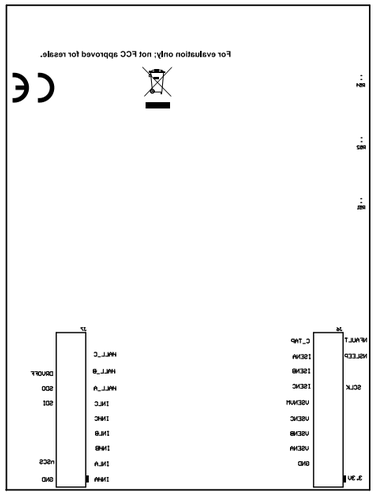
ALL ARTWORK VIEWED FROM TOP SIDE	BOARD № 70206	REV# A	SUM REV# Not in version control
LAYER NAME =	TID № N/A		
PLOT NAME = Signal Layer 2	GENERATED : 4/23/2024 5:19:11 PM		TEXAS INSTRUMENTS



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD N PCB06	REVs A	SUB REVs Not in version control
LAYER NAME = Bottom Layer	TIE N N/A		
PLOT NAME = Bottom Layer	GENERATED 4/29/2026 3:18:12 PM		TEXAS INSTRUMENTS

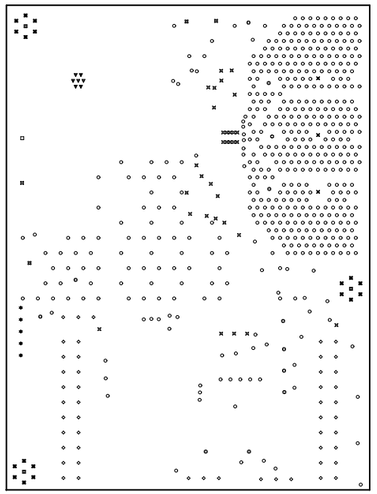


ALL ARTWORK VIEWED FROM TOP SIDE	BOARD N [illegible]	REV# A	SUN REV# Not in version control
LAYER NAME = Bottom Solder	TID N [illegible]		
PLOT NAME = Bottom Solder Mask	GENERATED [illegible] 4/23/2026	3:18:12 PM	TEXAS INSTRUMENTS

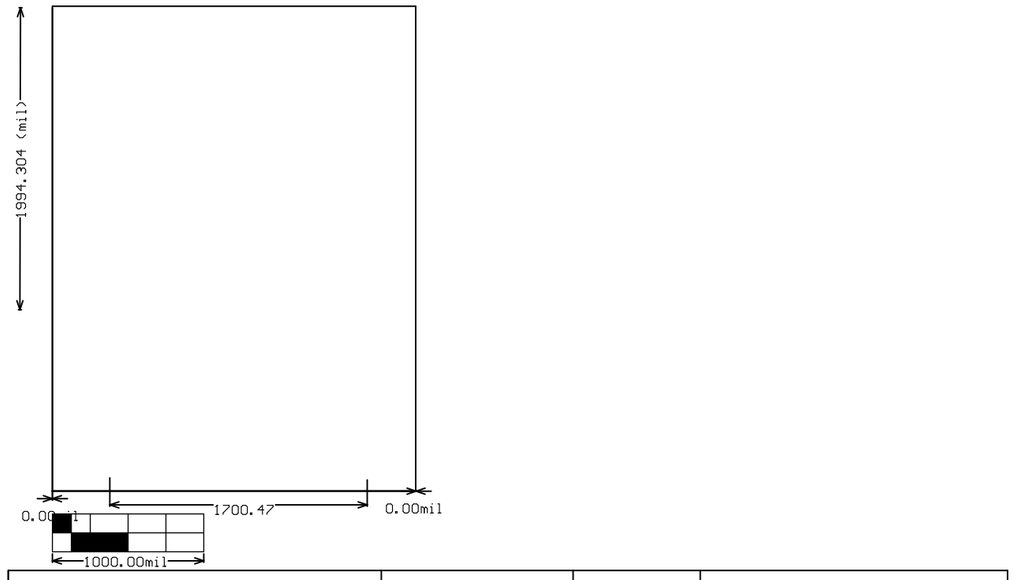


ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PDI036	REV: A	SVN REV: Not in version control
LAYER NAME = Bottom Overlay	TID #: N/A		
PLOT NAME = Bottom Overlay	GENERATED: 4/23/2026 5:18:12 PM		TEXAS INSTRUMENTS

Symbol	Quantity	Plated	Hole Size	Hole Tolerance
▼	4	PTH	0.0197"	±0.003"
■	10	PTH	0.0236"	±0.003"
●	114	PTH	0.0276"	±0.003"
■	6	PTH	0.0295"	±0.003"
◆	1	PTH	0.0315"	±0.003"
●	1	PTH	0.0354"	±0.003"
■	41	PTH	0.0394"	±0.003"
◆	1	PTH	0.0433"	±0.003"
■	1	PTH	0.0472"	±0.003"
■	1	PTH	0.0512"	±0.003"
■	1	PTH	0.0551"	±0.003"
■	1	PTH	0.0591"	±0.003"
	214			



ALL ARTWORK VIEWED FROM TOP SIDE		BOARD Rx	90006	REVs	A
LAYER NAME =	Drill Drawing	TED Rx	N/A		
PLOT NAME =	Drill Drawing	GENERATED =	4/23/2026	1:18:12 PM	TEXAS INSTRUMENTS



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: PCB06	REV: A	SUB REV: Not in version control
LAYER NAME = M2 Board Dimensions	TID #: N/A		
PLOT NAME = Board Dimensions	GENERATED: 4/23/2026 5:59:13 PM		TEXAS INSTRUMENTS